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STATEMENT UNDER 37 CFR 3.73(b)

Applicant/Patent Owner: Craig Bingert et al.

Application No./Patent No.: Not yet assigned Filed/Issue Date: 6/20/03

Entitled: Integrated Circuit And Associated Design Method Using Spare Gate Islands

Lattice Semiconductor Corporation, a Delaware Corporation

(Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. the assignee of the entire right, title, and interest; or

2. an assignee of less than the entire right, title and interest.

The extent (by percentage) of its ownership interest is _____ %
in the patent application/patent identified above by virtue of either:

A. An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel 013116, Frame 0422, or for which a copy thereof is attached.

OR

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The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

6/20/03

Date

503-268-8629

Telephone number

Mark L. Becker

Typed or printed name

Signature

Associate General Counsel, IP

Title

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